



KATIYAR ENGINEERING COLLEGE, KATIYAR

Under DSTTE, Government of Bihar

Affiliated to BEU Bihar, Approved by AICTE

M.TECH IN ELECTRONICS & COMMUNICATION (VLSI Design)

➤ About the Program

The main objective of this course to produce the professionals with wide knowledge in VLSI latest technologies and to enhance their skills in designing VLSI Chips for industrial requirements. The programme curriculum is aimed at VLSI system design, Hardware description Languages, System Architectures, Physical designs, Verification techniques, Low power design techniques, Mixed mode design methodologies, VLSI signal processing, Analog VLSI design, device modeling and fabrication, and MEMS. It also focuses to achieve excellence in research using the latest technologies.

ADMISSION 2023

ELIGIBILITY

- Passed Bachelor's or equivalent in the relevant field. Obtained at least 50% marks (45% marks in case of candidates belonging to reserved category) in the qualifying examination.
- Refer AICTE Notification F.No.27/RIFD/Pay/01/2017-18, date- 28.04.2017.
- Admission to MTech programs are based on merit of GATE(EC) score, in case of remaining seats based on merit of B.Tech Percentage.

M.TECH PROGRAMS OFFERED:

M.Tech in Electronics and Communication(VLSI Design) (18 Intake)

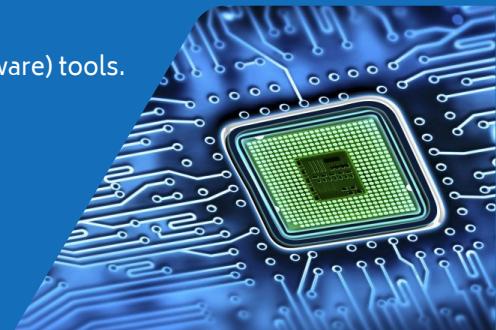
M.Tech is a two-year programme with core and advanced level courses and laboratory work followed by a one-year project with a strong research component.

FEATURES OF THIS PROGRAM

- The India Semiconductor Mission (ISM) an initiative by Govt of India for growth of VLSI will help to have more placement opportunities in India.
- The program includes hands-on experience on EDA (Software) tools.
- Experienced & Skilled Faculty.
- Advance Labs equipped.

Career Opportunities:

- Design engineer
- Application Engineer
- Verification engineer
- Fabrication Engineer
- CAD engineer
- Research & Development Engineer



For more details visit these links:

https://bceceboard.bihar.gov.in/pdf_Pro/PROS_PGEAC23.pdf

https://bceceboard.bihar.gov.in/pdf_Adv/ADV_PGEAC23_01.pdf

Important Dates for PGEAC-2023:

Online Registration starting date : 20.09.2023

Online Registration closing date : 26.09.2023 (10.00 PM)

Publication of Rank Card & Counselling Programme : 29.09.2023

Proposed date of Counselling : From 03.10.2023

For Enquiries, Please Contact

Dr. Ram Kumar (Ph.D, NET)

Assistant Professor & Head

Department of Electronics Engineering (VLSI Design)

Katiyar Engineering College, Katihar

Prominent Recruiters:

