



Ref. No. KEC/Exam/2025-79

Date 03/04/2025

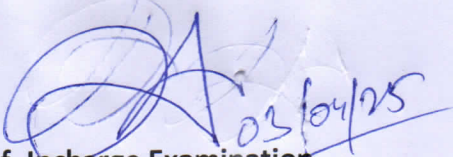
**M. TECH. MID -SEMESTER EXAMINATION - 2024**

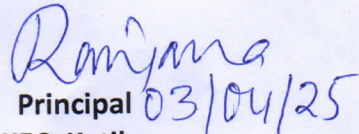
2nd Semester	2nd Shift 02:30 PM to 04:30 PM
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Date	M.Tech. in Electronics & Communication (VLSI design)
07.04.2025	Low Power VLSI Design
08.04.2025	Analog I C Design
09.04.2025	CAD For VLSI Circuit
11.04.2025	System on chip
12.04.2025	Soft Computing

**Copy to: For information and necessary preparation**

1. All HODs / Concerned subject faculty members
2. College Notice Board
3. P/I Academics
4. P/I Website

  
Prof. Incharge Examination  
KEC, Katihar

  
Principal  
KEC, Katihar